

Amendments to the Specification:

Please amend the Title of the Invention as follows:

**"DISPLAY DEVICE AND METHOD OF COMPENSATING PRIMARY IMAGE
DATA TO INCREASE A RESPONSE SPEED OF THE DISPLAY
~~IMAGE DATA COMPENSATION DEVICE AND METHOD AND~~
DISPLAY SYSTEM EMPLOYING THE SAME"**

Please replace the first paragraph on page 13 with the following amended paragraph:

The image signal source 100 includes a data processing part 110, a read only memory (ROM)~~synchronous dynamic random access memory (SDRAM)~~ 120, and a micro controller 130. The image signal source 100 outputs the primary gray-scale data RGB to the LCD device 200 for displaying images thereon, and outputs the compensation data 132 in response to temperature data 52 detected by and provided from a temperature sensor 50. The image signal source 100 is, for example, a computer, a signal processing block of a television receiver set, etc, electrically connected to the LCD device 200.

Please replace the third paragraph on page 13 with the following amended paragraph:

The ROMSARM 120 stores look-up tables (LUTs) of compensation data to optimize the response time of the LCD device 200. The LUTs are each associated with corresponding one of different temperature ranges. In other words, each LUT contains compensation data for a selected temperature range. The micro controller 130 selects an LUT of compensation data in response to the temperature data 52 provided from the temperature sensor 50 and outputs the selected LUT of compensation data to the LCD device 200.

Please replace the second full paragraph on page 15 with the following amended paragraph:

The LUT of compensation data stored in the first memory 220 is changed in response to variation of the ambient temperature which is sensed by the temperature sensor 50. The micro controller 130 reads out an appropriate LUT of compensation data from the ROMSDRAM 120 in response to the temperature data 52 provided from the temperature sensor 50, and the appropriate LUT of compensation data is stored in the firs memory 220. The timing control part 210 compensates the primary

gray-scale data using the appropriate LUT of compensation data to optimize the response time of the display system at the given ambient temperature. Further, for example, power of the LCD device 200 is controlled in response to the variation of the compensation data so as to prevent malfunction of a lamp of the LCD device. Also, an I²C bus (not shown) electrically connecting the micro controller 130 to the first memory 220 may be controlled to change the LUT of compensation data in the first memory 220.

Please replace the first full paragraph on page 16 with the following amended paragraph:

FIG. 6 is a block diagram illustrating the image signal source in FIG. 5 according to an exemplary embodiment of the present invention. Referring to FIG. 6, the image signal source 100 includes the data processing part 110, a first memory 120, a second memory 125, the micro controller 130, an analog-digital converter 135, and a voltage generating part 140. The first and second memories 120 and 125 are, for example, read only memories (ROMs) ~~synchronous dynamic random access memories (SDRAMs)~~.